



Attorney Docket No.: 73305.P019C2D

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor Application of:

Richard Maurice Barth
Matthew Murdy Griffin
Frederick Abbott Ware
Mark Alan Horowitz

Examiner: Barry, L.

Art Group: 2317

Serial No.: 08/667,293

Filed: June 19, 1996

For: A METHOD OF TRANSFERRING
DATA BY TRANSMITTING LOWER
ORDER AND UPPER ORDER
MEMORY ADDRESS BITS IN
SEPARATE WORDS WITH
RESPECTIVE OP CODES AND
START INFORMATION

Which is a continuation of application:

Serial No.: 08/484,917

Filed: June 6, 1995

Which is a divisional of application:

Serial No.: 08/381,015

Filed: January 30, 1995

Which is a continuation of application:

Serial No.: 07/848,421

Filed: March 6, 1992

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on September 25, 1996
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Name of Person Mailing Correspondence
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Signature 9/25/96
Date

Commissioner of
Patents and Trademarks
Washington, D.C. 20231

REQUEST TO APPROVE DRAWING CHANGES

Sir:

In accordance with 37 C.F.R. § 1.123, please find attached proposed new
Figures 4, 7a, 7b, 7c, and 7d showing changes in red ink. The proposed new drawings
correct informalities that were present in the drawings as originally filed.

In Figure 4, the processor device code Master[3:0] in bus cycles 4 and 5 has been amended in accordance with the specification at pages 9-12. In Figures 7a-7d, Count[7:2] includes six bits as described in the specification at pages 17-18. Therefore, the two left-most zeros in each of the entries of Count[7:2] have been deleted in accordance with the specification. Furthermore, in Figure 7b, the last entry of Count[1:0] has been amended from "0" to --00--. It is submitted that the amendments do not add new matter.

Please charge any shortages and credit any overages to Deposit Account No. 02-2666.

Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date: September 25, 1996 RBCT
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